

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: Jacunski et al.

Docket No.: BUR920020143US1

JAN 03 2005

Serial No.: 10/604,168

Art Unit: 2818

Filed: June 29, 2003

Examiner: Son L. Mai

Title: TIMER LOCKOUT CIRCUIT FOR SYNCHRONOUS APPLICATIONS

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents & Trademarks
P. O. Box 1450
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed on December 22, 2004, Applicants hereby provisionally elects Group II, claims 11-26, drawn to a SDRAM, classified in class 365, subclass 233. This election is made with traverse, and Applicants hereby reserve the right to file a divisional application in connection with unelected claims 1-10, drawn to a timer lockout circuit.

With regard to the Restriction Requirement, Applicants respectfully submit that the subject matter of all claims 1-26 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, Applicants respectfully submit that the search and the examination of the entire application could be made without serious burden. See MPEP § 803, in which it is stated that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits, even though it includes claims to independent or distinct inventions" (emphasis added). Applicants respectfully submit that this

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policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.

Should the Examiner require or request anything further from Applicant's prior to examination, the Examiner is requested to contact Applicants' undersigned representative at the Agent Direct Dial telephone number below. Otherwise, Applicants request early and favorable examination on the merits

Respectfully submitted,
FOR: Jacunski et al

Dated: 01/03/2005

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